Data Centers and High Performance Computing June 15th, 2016

Name:	Surname:	Student ID:
exposition. If you thi	ink a question is ambiguous, write yo d to use any note or digital device. I	s sheet, and use it as a folder to wrap the our interpretation and answer accordingly. If you are found copying or consulting notes,
Question 1 - (5 point Discuss the problem Directory-based coho	of cache coherency, comparing the	general idea of the Snooping cache and
multi-thread/multi-promust be developed to counter), and decent (vexecuted in parallel. Two versions of the interpretations of the interpretation of the interpretati	s of wait-free and lock-free progocess application, project and develor access this data structure, namely which subtracts 1 from the current variations.	ress conditions. Then, in the context of a elop an <i>incremental counter</i> . Two operations in (which adds 1 to the current value of the lue of the counter). Operations must be safely mented: a <i>wait-free</i> and a <i>lock-free</i> one. To this
	<u>#</u>	
•	_	of the two implementations. The student can aguage, or in pseudocode.
	ed Transactional Memory (RTM) facil	ity offered by modern Hardware Transactional the pros and cons from a development point of
Question 4 - (5 points in the context of MPI kind of abstraction	•	what are the programmatical benefits of this
Question 5 - (5 points of the	•	nain differences between different types of
I, the undersigned, website the results		675/96, allow the teachers to publish on the
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